

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L2    | 118  | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |
| L6    | 118  | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |
| L9    | 51   | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3 and (PLL or phase near3 loop)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |
| L10   | 51   | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3 and (PLL or phase near3 loop)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |
| L12   | 157  | digital near2 processor and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (filter\$1 or downstream) and charge adj pump  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:52 |
| L13   | 218  | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and charge adj pump and (filter\$1)       | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |
| L14   | 61   | L13 not L12  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |
| L16   | 66   | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (pre-compensat\$4 or precompensat\$4) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/07/06 16:06 |

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| L17 | 232 | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and group near2 delay   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L18 | 203 | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (group near2 delay) and compensat\$4                        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L19 | 0   | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (group near2 delay) and (re-compensat\$4 or recompensat\$4) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L20 | 0   | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (chain near5 delay) and (re-compensat\$4 or recompensat\$4) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L21 | 118 | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L22 | 218 | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and charge adj pump and (filter\$1)                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L23 | 232 | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and group near2 delay   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L24 | 203 | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (group near2 delay) and compensat\$4                        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/07/06 16:06 |

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| L25 | 0   | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (group near2 delay) and (re-compensat\$4 or recompensat\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L26 | 0   | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (chain near5 delay) and (re-compensat\$4 or recompensat\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L27 | 51  | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3 and (PLL or phase near3 loop)  | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L28 | 51  | digital near2 processor and (DAC or D/A or digital adj analog) and pre-compensat\$3 and (PLL or phase near3 loop)  | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L29 | 157 | digital near2 processor and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (filter\$1 or downstream) and charge adj pump                        | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L30 | 61  | L22 not L29  | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |
| L31 | 66  | (digital near2 processor or DSP) and (DAC or D/A or digital adj analog) and (PLL or phase adj locked adj loop) and (pre-compensat\$4 or precompensat\$4)                       | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/07/06 16:06 |